

Module Computer Systems weeks 3 and 4

Computer Architecture and Organisation reading guide

Reading Guide

(note: 'M&H x' refers to section x in the book Computer Architecture and Organization, Murdocca and Heuring)

Lecture 1: Introduction to Computer Architecture / Busses, Peripherals and I/O

M&H 1.1

M&H 4.1 until M&H 4.3

M&H 8.1 until M&H 8.4

Lecture 2: Interrupts

M&H 5.3.3

Lecture 3: Memory

M&H 7.1 until 7.2

Lecture 4: Memory Modules

M&H 7.3

Lecture 5: Caches

M&H 7.5

Lecture 6: Caches (continued)

M&H 7.5

Lecture 7: Diagnostic Test

Furthermore, in the lecture slides, additional material will be presented. This additional material is part of this module and will therefore be part of the CAO exam.